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Hybrid charge division multiplexing method for silicon photomultiplier based PET detectors

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Abstract
Silicon photomultiplier (SiPM) is widely utilized in various positron emission tomography (PET) detectors and systems. However, the individual recording of SiPM output signals is still challenging owing to the high granularity of the SiPM; thus, charge division multiplexing is commonly used in PET detectors. Resistive charge division method is well established for reducing the number of output channels in conventional multi-channel photosensors, but it degrades the timing performance of SiPM-based PET detectors by yielding a large resistor–capacitor (RC) constant. Capacitive charge division method, on the other hand, yields a small RC constant and provides a faster timing response than the resistive method, but it suffers from an output signal undershoot. Therefore, in this study, we propose a hybrid charge division method which can be implemented by cascading the parallel combination of a resistor and a capacitor throughout the multiplexing network. In order to compare the performance of the proposed method with the conventional methods, a 16-channel Hamamatsu SiPM (S11064-050P) was coupled with a \(4 \times 4\) LGSO crystal block \((3 \times 3 \times 20\text{mm}^3)\) and a \(9 \times 9\) LYSO crystal block \((1.2 \times 1.2 \times 10\text{mm}^3)\). In addition, we tested a time-over-threshold (TOT) readout using the digitized position signals to further demonstrate the feasibility of the time-based readout of multiplexed signals based on the proposed method. The results indicated that the proposed method exhibited...
good energy and timing performance, thus inheriting only the advantages of conventional resistive and capacitive methods. Moreover, the proposed method showed excellent pulse shape uniformity that does not depend on the position of the interacted crystal. Accordingly, we can conclude that the hybrid charge division method is useful for effectively reducing the number of output channels of the SiPM array.

Keywords: silicon photomultiplier (SiPM), positron emission tomography (PET), charge division multiplexing, hybrid charge division method, time-based readout, time-over-threshold (TOT)

(Some figures may appear in colour only in the online journal)

1. Introduction

Silicon photomultiplier (SiPM) is widely utilized in current positron emission tomography (PET) detectors and systems owing to its compactness, low bias voltage operation, high amplification gain, high photon detection efficiency, and magnetic field insensitivity (Lee and Hong 2010, Kwon et al 2011, Roncali and Cherry 2011, Hong et al 2012, Yamamoto et al 2012, Yoon et al 2012, Lee and Lee 2015, Grant et al 2016, Ko et al 2016). In particular, SiPMs combined with fast and bright scintillation crystals have allowed PET detectors to achieve a more precise time-of-flight measurement (Schaart et al 2010, Nemallapudi et al 2015, Cates and Levin 2016, Ullah et al 2016), and to enhance the gain of signal-to-noise ratio in PET images (Moses 2007, Conti 2011, Lee 2015, Son et al 2016, Surti and Karp 2016).

Theoretically, the individual signal readout of the SiPMs results in the best performance of the PET detectors, but the high granularity of the SiPM makes it difficult to record the output signals individually from each SiPM channel, especially at the level of a full-ring PET system.

One possible approach to resolve this problem is the use of application-specific integrated circuits (ASICs) which enable the management of the SiPM output signals in a highly compact manner; however, this approach demands a large amount of time and resources during the developmental stage (Anghinolfi et al 2004, Corsi et al 2009, Fischer et al 2009, Shen et al 2012, Rolo et al 2013). Another possible approach is to multiplex the SiPM signals using discrete electronic components. Although this approach slightly compromises the performance of the detector as compared to the ASIC-based method, it is an excellent alternative to reading out the SiPM signals thanks to the simple circuit design and low developing cost.

Charge division multiplexing is a well-established technique for reducing the number of output channels of the PET detectors. The resistive charge division method is a conventional method, which results in moderate performance of the detector with a good position decoding accuracy (Siegel et al 1996, Olcott et al 2005a, Popov and Majewski 2006). However, multiplexing SiPM signals using the resistive charge division network causes significant resistance–capacitance delay (RC delay) in the scintillation signals, as depicted in figure 1(a), thereby worsening the timing performance of the detector (Ko et al 2015). The capacitive charge division method is an alternative technique, which mitigates the undesirable influence of RC delay by using a minimal number of resistive components throughout the multiplexing network (Olcott et al 2005b, Downie et al 2013, Du et al 2013). Although the capacitive charge division method achieves a faster timing performance of the detector, it suffers from an output signal undershoot, as depicted in figure 1(b), causing a degradation of energy performance and a poor quality of flood histogram.
In order to address the aforementioned problems, we propose an improved charge division method, in this study called hybrid charge division method, which effectively reduces the large number of SiPM channels without degrading the pulse shape of the output signal by cascading the parallel combination of a resistor and a capacitor throughout the multiplexing network, as depicted in figure 1(c). In order to demonstrate the feasibility of the proposed method, we built two different detector configurations using a 16-channel SiPM coupled with lutetium-based crystal blocks: a one-to-one coupled lutetium gadolinium oxyorthosilicate (LGSO)/SiPM detector and a light-shared lutetium yttrium oxyorthosilicate (LYSO)/SiPM detector. The performance of the detector was subsequently compared with that of the conventional resistive and capacitive charge division methods in terms of energy resolution, coincidence resolving time (CRT), quality of the flood histogram, and the pulse shape of the multiplexed output signal. For a fair comparison, the passive electronic components of each charge division network were chosen carefully to yield an identical amount of output charge within the same integration window. In addition, we tested a time-over-threshold (TOT) readout scheme using the digitized position signals to further demonstrate the feasibility of the time-based readout of multiplexed signals based on the hybrid charge division method.

2. Materials and methods

2.1. Hybrid discretized positioning circuit

In the aspect of circuit implementation, hybrid charge division method can be applied to the various kinds of charge division networks, including discretized positioning circuit (DPC), Anger logic, row-column sum, symmetric charge division (Song et al 2010, Majewski 2011, Schmall et al 2012, Goertzen et al 2013, Ko et al 2013, Kwon and Lee 2014), by simply replacing the resistors or the capacitors in the original circuits with well-selected resistor–capacitor pairs. In this study, as the first feasibility test of hybrid charge division method, we applied this new concept to the DPC network because the DPC is a commonly-used charge division network in PET detectors and requires a much smaller number of electronic components than other charge division networks.
We implemented resistive, capacitive and hybrid DPCs in combination with a 16-channel SiPM, as shown in figures 2(a)–(c), and performed a comparative evaluation of the proposed method. First, we started from the analysis of the approximated first-order frequency response of the propagating signal of each resistive, capacitive and hybrid DPC using the simplified SiPM model: \( I_d \) corresponds to the current source in the SiPM circuit model generated based on the GATE optical simulation, \( R_h \) and \( C_h \) correspond to the resistor and the capacitor mounted on the hybrid DPC, respectively, \( R_a \) is a shunt resistor directly connected to the anode of SiPM, \( C_d \) is a terminal capacitance of a single SiPM channel, \( v_1(s) \) is the scintillation signal generated from a discharged SiPM, \( v_2(s) \) is the propagating signal recorded at the adjacent node of the discharged SiPM. The scale factors for the parameters \( I_d \), \( C_d \), and \( R_a \) were taken into account (1/2 for \( I_d \) and \( C_d \), 2 for \( R_a \)) in order to equally distribute the impedance for each lumped section of the DPC network.

Figure 2. (a) Conceptual diagram of a 4 × 4 resistive DPC coupled to 16-channel SiPM. (b) Conceptual diagram of a 4 × 4 capacitive DPC coupled to 16-channel SiPM. (c) Conceptual diagram of a 4 × 4 hybrid DPC coupled to 16-channel SiPM. (d) Lumped \( \pi \)-model of a 4 × 4 hybrid DPC using simplified SiPM model: \( I_d \) corresponds to the current source in the SiPM circuit model generated based on the GATE optical simulation, \( R_h \) and \( C_h \) correspond to the resistor and the capacitor mounted on the hybrid DPC, respectively, \( R_a \) is a shunt resistor directly connected to the anode of SiPM, \( C_d \) is a terminal capacitance of a single SiPM channel, \( v_1(s) \) is the scintillation signal generated from a discharged SiPM, \( v_2(s) \) is the propagating signal recorded at the adjacent node of the discharged SiPM. The scale factors for the parameters \( I_d \), \( C_d \), and \( R_a \) were taken into account (1/2 for \( I_d \) and \( C_d \), 2 for \( R_a \)) in order to equally distribute the impedance for each lumped section of the DPC network.

In the SiPM-based PET detectors, it is known that the resistive DPC yields a large RC constant due to the accumulated terminal capacitance of SiPM array, as depicted in figure 1(a), thus showing a significant degradation of timing performance. Equation (1) analytically demonstrates that the propagating SiPM signal of the resistive DPC, \( v_{2, \text{resistive}}(s) \), is significantly affected by the pole and its influence increases as the terminal capacitance of the photosensor \((C_d)\) increases. Moreover, it can be inferred that the position signals of the resistive DPC would...
result in a discrepant observation since each position signal undergoes a different amount of RC delay depending on the distance between the discharged SiPM and the readout channel.

\[
v_{2,\text{resistive}}(s) = \frac{\left(2R_a \parallel \frac{1}{s(C_d/2)}\right)}{R_h + \left(2R_a \parallel \frac{1}{s(C_d/2)}\right)} = \frac{2R_a}{R_h + 2R_a} \left[\frac{1}{1 + s\frac{R_a R_h C_d}{R_h + 2R_a}}\right] v_1(s). \quad (1)
\]

In principle, the DPC can also be implemented using a capacitive mesh, as shown in figure 2(b). Equation (2) analytically demonstrates that the capacitive DPC behaves as a cascaded first-order passive high pass filter; consequently, the propagating SiPM signal, \(v_{2,\text{capacitive}}(s)\), can preserve high-frequency components while exhibiting the undershoot response inherited from the DC blocking nature of the capacitor, as depicted in figure 1(b). In this regard, the capacitive charge division method could yield a faster timing performance than the resistive method, with slight performance degradation of the signal-to-noise ratio of the multiplexed output signal.

\[
v_{2,\text{capacitive}}(s) = \frac{\left(2R_a \parallel \frac{1}{s(C_d/2)}\right)}{\frac{1}{R_h} + \left(2R_a \parallel \frac{1}{s(C_d/2)}\right)} = \left[\frac{s(2R_a C_h)}{1 + s(2R_a C_h + R_c C_d)}\right] v_1(s). \quad (2)
\]

The analytical description of the propagating SiPM signal of the hybrid DPC is shown in equation (3). In our proposed method, if the selected values of the resistor \((R_h)\) and the capacitor \((C_h)\) satisfy the relation given in equation (4), the propagating SiPM signal, \(v_{2,\text{hybrid}}(s)\), would exhibit excellent pulse shape uniformity, showing identical rise and fall times regardless of the position of the discharged SiPM, as depicted in figure 1(c). In other words, the hybrid charge division method can eliminate the undesirable position-dependent signal distortion observed in the conventional resistive and capacitive charge division methods by cancelling the pole-zero pair of the detector system, as illustrated in equation (5).

\[
v_{2,\text{hybrid}}(s) = \frac{\left(2R_a \parallel \frac{1}{s(C_d/2)}\right)}{\frac{1}{R_h} + \left(2R_a \parallel \frac{1}{s(C_d/2)}\right)} = \frac{2R_a}{R_h + 2R_a} \left[\frac{1 + sR_h C_h}{1 + s\frac{R_h R_a (C_d + 2C_h)}{R_h + 2R_a}}\right] v_1(s) \quad (3)
\]

\[
R_h C_h = \frac{R_h R_a (C_d + 2C_h)}{R_h + 2R_a} \quad (4)
\]

\[
v_{2,\text{hybrid}}(s) = \frac{2R_a}{R_h + 2R_a} v_1(s). \quad (5)
\]

### 2.2. Simulation study

Based on the above circuit analysis, we performed simulation via the Personal Simulation Program with Integrated Circuit Emphasis (PSPICE; Capture CIS, Cadence Design Systems Inc., US) to investigate the feasibility of the hybrid DPC.

We used the equivalent circuit model of a Hamamatsu SiPM with the terminal capacitance \((C_d)\) of 320 pF to obtain reliable simulation results (Seifert et al 2009, Avella et al 2012). The current source in the SiPM circuit model was generated based on the Geant4 application for tomographic emission (GATE) optical simulation (OpenGATE collaboration, France). The...
detector response of the single SiPM channel was obtained by the convolution of the single micro-cell response of the SiPM and the light photon distribution of the lutetium oxyorthosilicate (LSO) crystal.

We repeated the simulation with various different sets of $R_h$ and $C_h$ pairs that satisfy the equation (4) until we could find the most preferable condition for a fair comparison between the conventional and the proposed methods. The shunt resistor ($R_s$) in the bias circuit was carefully chosen not to yield a large amount of charge loss from the anode of the SiPM to the ground. Owing to the symmetrical structure of the DPC network, the position signals from only one quadrant of the SiPM array were sampled and examined. Gaussian noise with a standard deviation of 2 mV (i.e. similar to the value observed in a real measurement) was added to each position signal. The flood histogram was generated using the position decoding logic, as described in equations (6) and (7), where $Q_A$, $Q_B$, $Q_C$, and $Q_D$ correspond to the amount of output charge collected from each position signal A, B, C, and D, respectively.

$$X = \frac{Q_A + Q_B - Q_C - Q_D}{Q_A + Q_B + Q_C + Q_D}$$

(6)

$$Y = \frac{Q_A - Q_B - Q_C + Q_D}{Q_A + Q_B + Q_C + Q_D}.$$  

(7)

2.3. Detector configurations

The performance of the DPC detector was tested using two different configurations: the one-to-one coupled LGSO/SiPM detector and the light-shared LYSO/SiPM detector. In the former configuration, a 16-channel old version of Hamamatsu SiPM (S11064-050P; Hamamatsu Photonics K.K., Japan) was coupled with a $4 \times 4 \times 3 \times 3 \times 20$ mm$^3$ LGSO crystal block (Lu$_{1.9}$Gd$_{0.1}$SiO$_4$(Ce); Hitachi Chemical, Japan), whereas in the latter configuration, a $9 \times 9$ array of $1.2 \times 1.2 \times 10$ mm$^3$ LYSO crystal block (Lu$_{1.8}$Y$_{0.2}$SiO$_5$(Ce); SIPAT, China) was coupled to the abovementioned SiPM using a 1.85 mm thick PVC light guide. In order to maximize the efficiency of the light collection, all the crystals were wrapped with a 0.065 mm thick enhanced spectral reflector (ESR; 3M, US) and both the crystal blocks were tightly coupled using an optical grease (BC-630; OKEN, Japan) with a refractive index of 1.465.

2.4. Measurement setup and data acquisition

Figure 3 shows the measurement setup used in this study. The experiments were conducted inside a temperature-controlled box at 20 °C. The bias voltage supplied to the SiPM was 71.7 V (with an overvoltage of approximately 2.0 V). We used a reference detector consisting of Hamamatsu R9800 PMT and an ESR-wrapped single $4 \times 4 \times 10$ mm$^3$ LYSO crystal to acquire the coincidence data. The single timing resolution of the reference detector was 240.4 ps, which was measured using the system described by Lee et al (2011). A $^{22}$Na point source (15 $\mu$Ci) with a diameter of 0.25 mm was deployed in front of the reference detector to irradiate the opposite detector uniformly with a sufficiently large solid angle. The distance between the two detectors was 15 cm.

Figure 4 illustrates a detailed schematic of the DPC amplifier board. The output signals from the DPC network were processed using high-speed current feedback amplifiers (AD8000; Analog Device, US). The input impedance of the amplifier was chosen to be 50 $\Omega$. The multiplexed output signals (i.e. from the nodes A, B, C, and D) were initially amplified to obtain the energy and position information, and the four amplified signals were subsequently
transmitted to the second-stage summing amplifier to generate a Sum signal for obtaining the timing information. A passive pole-zero cancellation filter was implemented at the terminal node of the second-stage summing amplifier to minimize the baseline fluctuation owing to the dark noise. Impedance matching was also performed to avoid signal reflection from the load.

After the signal amplification stage, each amplified output signal was fed into a domino-ring-sampler (DRS4) chip based waveform digitizer (DT5742B; CAEN, Italy), which has a 12-bit sampling resolution with a maximum sampling rate of 5 Giga-Samples s\(^{-1}\). Simultaneously, both the Sum signal and the reference PMT signal were split into two routes by the fan-in/fan-out module (N401; CAEN, Italy): one is connected to the waveform digitizer.

**Figure 3.** Measurement setup.

**Figure 4.** Detailed schematic of the DPC amplifier board. (a) First stage non-inverting amplifier for generating position and energy information. (b) Second stage summing amplifier for generating the trigger signal.
for the time pick-off, and the other is connected to the constant fraction discriminator (CFD) modules (N843; CAEN, Italy) for generating trigger signals. The digital outputs generated by each CFD nuclear instrumentation module (NIM) were subsequently fed into the AND NIM (N455; CAEN, Italy) for coincidence detection. The coincidence window was set as 20 ns. The coincidence digital output from the AND NIM was connected to the fast trigger port of the waveform digitizer. All the digitized signals from the waveform digitizer were transmitted through a universal serial bus (USB) 2.0 cable to a personal computer for data analysis.

2.5. Data analysis and performance evaluation

The performance of the hybrid DPC detector was investigated via a comparison study with the resistive and capacitive DPCs. For a fair comparison, the passive electronic components in each DPC network were chosen based on the results of the simulation study to yield an identical amount of output charge within the same integration window.

In order to show the multiplexed output signals recorded from each DPC network, the representative pulse shapes of position signals (i.e. A, B, C, and D) were sampled from the corner and the center crystals at a rate of 1 Giga-Samples s$^{-1}$. Only 100 events within the 1% energy window at photopeak were included and averaged to exclusively visualize the pulse shape of the 511 keV scintillation signal.

The quality of the flood histogram, energy resolution, and CRT were measured for each crystal element in order to evaluate the performance of the detector. As a quality parameter of the flood histogram, the distance-to-width ratio (DWR) was calculated as the average ratio between the distance of the adjacent peaks on the flood histogram and the average full width at half maximum (FWHM) value of the peaks, as described in equation (8) (Won et al 2016). In the equation (8), $x_i$, $x_j$ and $y_i$, $y_j$ correspond to the position of the $i$th and $j$th adjacent crystal pairs along the horizontal axis ($x$-axis) and the vertical axis ($y$-axis) on the flood histogram, respectively. $w_{xi}$, $w_{xj}$, $w_{yi}$, $w_{yj}$ are the FWHM values of 1D projection profiles along the $x$- and $y$-axis of the $i$th and $j$th crystal, respectively. $N_{adj}$ is the total number of adjacent crystal pairs

$$DWR = \frac{1}{N_{adj}} \sum_{i,j \in (adj \ pair)} \left( \frac{|x_i - x_j|}{(w_{xi} + w_{xj})/2} + \frac{|y_i - y_j|}{(w_{yi} + w_{yj})/2} \right). \quad (8)$$

In order to calculate the energy resolution, the total amount of output charge was estimated by summing the integrated charge of each position signal. The charge integration was performed during an interval of 150 ns. The event-by-event baseline correction was done using the mean value of 30 data points before the onset of the signal.

In the timing measurement, only the events within the energy window of full width at tenth maximum (FWTM) at 511 keV photopeak were taken into account. The arrival time of the incident gamma photon was picked off from the Sum signal by using the digital CFD with a threshold level of 5% (i.e. 5% of peak amplitude of each Sum signal). Each Sum signal was oversampled ($\times 10$) using cubic spline interpolation to minimize the quantization error. The CRT of the DPC detectors was subsequently calculated by quadratically subtracting the single time resolution of the reference detector ($\delta t_{ref}$) from the CRT between the DPC and reference detectors ($\Delta t_{DPC/Ref}$) and multiplying the result with $\sqrt{2}$, as described in equation (9) (Ko and Lee 2015)

$$CRT = \sqrt{2} \cdot \sqrt{\Delta t^2_{DPC/Ref} - \delta t^2_{Ref}}. \quad (9)$$
In order to quantitatively analyze the degree of discrepancy among the four position signals from each DPC network, coefficient of variation of a rise time (CV_{\tau_r}) and coefficient variation of fall time variation (CV_{\tau_f}) were defined as figure-of-merits and calculated as a percent standard deviation of the rise time (\sigma_{\tau_r}) and the fall time (\sigma_{\tau_f}) of a certain position signal with respect to the average rise time (\mu_{\tau_r}) and the average fall time (\mu_{\tau_f}) of position signals interacted throughout the entire crystal block, as described in equation (10) and equation (11), respectively. A smaller value of CV_{\tau_r} and CV_{\tau_f} indicate that the DPC detector yields a smaller distortion of the position signal throughout the entire crystal block. Owing to the symmetric structure of the DPC network, the rise time and the fall time of only a single position signal (i.e. A signal) per each crystal were plotted and presented. The rise time (\tau_r) and the fall time (\tau_f) were defined as the time elapsed between 10% and 90% of the signal amplitude at a rising slope, and the time required to decrease from 90% to 10% of the signal amplitude at a falling slope, respectively (Yeom et al. 2013)

\[ CV_{\tau_r} = \frac{\sigma_{\tau_r}}{\mu_{\tau_r}} \times 100 \]  
\[ CV_{\tau_f} = \frac{\sigma_{\tau_f}}{\mu_{\tau_f}} \times 100. \]

In order to further demonstrate the feasibility of the time-based readout of the hybrid charge division method, we tested the TOT readout scheme using the digitized position signals recorded from the one-to-one coupled LGSO/SiPM detector for each DPC network. Each of the TOT data was encoded by a digital leading edge discriminator (LED) with a fixed threshold of 75 mV. For the calibration of the TOT data, nonlinear least-squares regression was performed to find the best-fit curve of the TOT against the integrated charge of the position signal. In this case, we employed a logarithmic regression model since the TOT has a strong logarithmic correlation with the integrated charge of the position signal. The linearized TOT data were subsequently used to generate the flood histogram. Owing to the symmetric structure of the DPC network, only a single position signal (i.e. D signal) which interacted along the crystal elements in the diagonal direction (i.e. D-B direction in the SiPM array as depicted in figure 8) was used for the representative graphical visualization of the TOT against the integrated charge of the position signals.

3. Result

3.1. Simulation result

Figure 5 shows the simulation result of the flood histogram and position signals of the hybrid DPC sampled from one quadrant array of the SiPM. As shown in figure 5(a), all 16 crystals were clearly resolved on the flood histogram even under the existence of Gaussian noise. The position signals depicted in figure 5(b) showed excellent pulse shape uniformity irrespective of the position of the interacted crystal.

3.2. Experimental result: charge-based readout

3.2.1. One-to-one coupled LGSO/SiPM detector. As shown in figures 6(a)–(c), all 16 crystals were clearly resolved on the flood histograms with excellent DWR values of 25.6, 24.7, and 25.7 for the resistive, capacitive, and hybrid DPCs, respectively. The capacitive DPC yielded a relatively small DWR value although the dynamic range of the flood histogram was similar in
all the DPC networks, since the capacitive charge division method is slightly more vulnerable to the high-frequency noise than the resistive and hybrid charge division methods.

Figures 6(d)–(i) show the 100-fold averaged position signal for each DPC network interacted in both the corner and the center crystals. In the case of the resistive DPC, as expected, different extents of RC delay were observed among the four position signals. The capacitive DPC yielded steeper position signals in both the corner and center crystals as compared to the resistive DPC, but exhibited an undershoot response. The hybrid DPC, on the other hand, yielded uniform position signals throughout the entire crystal block without resulting in the undesirable signal distortion.

Figures 6(j)–(o) quantitatively visualize the degree of pulse shape uniformity for each DPC network. The hybrid DPC yielded the smallest CV value of 4.0% (τ\text{min} = 31.5 ns, \tau\text{max} = 35.3 ns) and CV value of 1.1% (τ\text{min} = 635.4 ns, \tau\text{max} = 654.9 ns), whereas the resistive and capacitive DPCs yielded relatively larger CV values of 20.9% (τ\text{min} = 23.9 ns, \tau\text{max} = 52.3 ns) and 14.3% (τ\text{min} = 28.3 ns, \tau\text{max} = 45.9 ns); and relatively larger CV values of 4.1% (τ\text{min} = 642.7 ns, \tau\text{max} = 732.7 ns) and 64.6% (τ\text{min} = 100.3 ns, \tau\text{max} = 670.0 ns), respectively.

Table 1 summarizes the result of the performance evaluation. A similar energy resolution was achieved for the resistive, capacitive, and hybrid DPCs with values of 10.7%, 10.9%, and 10.7%, respectively. In the timing measurement, both the capacitive and hybrid DPCs yielded a good CRT of 388 ps, whereas the resistive DPC yielded a CRT of 401 ps.

3.2.2. Light-shared LYSO/SiPM detector. Figure 7 shows the result of the light-sharing experiment. The flood histograms in figures 7(a)–(c) show that all 81 crystals were clearly resolved in the resistive, capacitive, and hybrid DPCs with DWR values of 2.71, 2.66, and 2.75, respectively; even though the crystal elements were almost three times smaller than those used for the one-to-one coupling experiment.
Further, similar trends were observed in the pulse shape. The hybrid DPC yielded the most homogeneous position signals throughout the entire crystal block, whereas both the resistive and capacitive DPCs exhibited position-dependent signal distortion, as shown in figures 7(d)–(i).

Similarly, the CV$_{\tau_r}$ values for the resistive, capacitive, and hybrid DPCs were 11.3% ($\tau_{\text{min}}$: 33.6 ns, $\tau_{\text{max}}$: 55.1 ns), 7.9% ($\tau_{\text{min}}$: 34.7 ns, $\tau_{\text{max}}$: 47.4 ns), and 3.3% ($\tau_{\text{min}}$: 37.0 ns, $\tau_{\text{max}}$: 42.5 ns), as shown in figures 7(j)–(l), respectively; and the CV$_{\tau_f}$ values for the resistive, capacitive, and hybrid DPCs were 2.6% ($\tau_{\text{min}}$: 635.3 ns, $\tau_{\text{max}}$: 707.8 ns), 41.8% ($\tau_{\text{min}}$: 145.7 ns, $\tau_{\text{max}}$: 669.0 ns), and 0.4% ($\tau_{\text{min}}$: 673.5 ns, $\tau_{\text{max}}$: 687.2 ns), as shown in figures 7(m)–(o), respectively;

Table 2 shows the results of the performance evaluation. A similar energy resolution was achieved for the resistive, capacitive, and hybrid DPCs with values of 15.7%, 15.7%, and 15.6%, respectively. In the case of the timing performance, CRT values of 596 ps, 556 ps, and 555 ps were achieved for the resistive, capacitive, and hybrid DPCs, respectively.

#### Table 1. Performance evaluation results of one-to-one coupling experiment.

<table>
<thead>
<tr>
<th>Detector configuration</th>
<th>DWR (a.u.)</th>
<th>$dE/E$ (%)</th>
<th>CRT (ps FWHM)</th>
<th>CV$_{\tau_r}$ (%)</th>
<th>CV$_{\tau_f}$ (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>One-to-one</td>
<td>Resistive</td>
<td>25.6</td>
<td>10.7 ± 0.5</td>
<td>401 ± 35</td>
<td>20.9</td>
</tr>
<tr>
<td></td>
<td>Capacitive</td>
<td>24.7</td>
<td>10.9 ± 0.6</td>
<td>388 ± 18</td>
<td>14.3</td>
</tr>
<tr>
<td></td>
<td>Hybrid</td>
<td>25.7</td>
<td>10.7 ± 0.5</td>
<td>388 ± 17</td>
<td>4.0</td>
</tr>
</tbody>
</table>

$^{a}dE/E$: energy resolution.

Further, similar trends were observed in the pulse shape. The hybrid DPC yielded the most homogeneous position signals throughout the entire crystal block, whereas both the resistive and capacitive DPCs exhibited position-dependent signal distortion, as shown in figures 7(d)–(i).

Figure 6. Experimental results of one-to-one coupled LGSO/SiPM detector. Flood histograms generated using the (a) resistive, (b) capacitive, and (c) hybrid DPCs. The 100-fold averaged position signals generated in the corner crystal on (d) resistive, (e) capacitive, and (f) hybrid DPCs, and in the center crystal on (g) resistive, (h) capacitive, and (i) hybrid DPCs. Per-crystal rise times of position signal A from the (j) resistive, (k) capacitive, and (l) hybrid DPCs. Per-crystal fall times of position signal A from the (m) resistive, (n) capacitive, and (o) hybrid DPCs.

Table 1. Performance evaluation results of one-to-one coupling experiment.

<table>
<thead>
<tr>
<th>Detector configuration</th>
<th>DWR (a.u.)</th>
<th>$dE/E$ (%)</th>
<th>CRT (ps FWHM)</th>
<th>CV$_{\tau_r}$ (%)</th>
<th>CV$_{\tau_f}$ (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>One-to-one</td>
<td>Resistive</td>
<td>25.6</td>
<td>10.7 ± 0.5</td>
<td>401 ± 35</td>
<td>20.9</td>
</tr>
<tr>
<td></td>
<td>Capacitive</td>
<td>24.7</td>
<td>10.9 ± 0.6</td>
<td>388 ± 18</td>
<td>14.3</td>
</tr>
<tr>
<td></td>
<td>Hybrid</td>
<td>25.7</td>
<td>10.7 ± 0.5</td>
<td>388 ± 17</td>
<td>4.0</td>
</tr>
</tbody>
</table>

$^{a}dE/E$: energy resolution.

Further, similar trends were observed in the pulse shape. The hybrid DPC yielded the most homogeneous position signals throughout the entire crystal block, whereas both the resistive and capacitive DPCs exhibited position-dependent signal distortion, as shown in figures 7(d)–(i).
3.3. Experimental result: time-based readout

Figures 8(a)–(c) illustrate the scatter plot of the TOT versus the integrated charge of the position signal, showing the best-fit logarithmic curve for each DPC network. In the resistive DPC, the TOT showed a good correlation to the integrated charge of the position signal with a coefficient of determination ($R^2$) of 0.9343 and a root mean square error (RMSE) of 23.8; however, the correlation had become relatively poor as the amount of integrated charge increased. In the capacitive DPC, the regression yielded a poor correlation between the TOT and integrated charge with an $R^2$ of 0.6948 and an RMSE of 135.1. In the case of the hybrid DPC, on the other hand, the TOT and integrated charge exhibited a strong logarithmic correlation, yielding the lowest RMSE of 13.2 and the highest $R^2$ of 0.9716 among the three methods, even when a large amount of output charge was collected.

Figures 8(d)–(f) show the flood histograms generated from the linearized TOT data of each position signal. Among them, the hybrid DPC yielded the best quality of the flood histogram, showing the smallest pixel size; whereas both the resistive and capacitive DPCs showed a significant distortion of the flood histogram, especially on the periphery of the crystal block.

4. Discussion

In PET, it is important to achieve a precise timing measurement for better localization of the incident gamma photon. In this regard, the capacitive charge division method is preferred for the development of the SiPM-based PET system owing to its fast temporal response, as
demonstrated in section 2.1. Our experimental results showed that the hybrid charge division method yielded similar (or slightly better) timing performance as compared to the capacitive method, as summarized in tables 1 and 2. A possible reason for the good timing performance is the minimal RC delay of output signal, which was achieved by cancelling out the undesirable poles and zeros of detector system. The results are consistent with previous studies (Ko et al 2013, Du et al 2013, Won et al 2016), in that a steeper rising slope of output signal can minimize the variation of the arrival time of the incident gamma photon. Therefore, the hybrid charge division method can be beneficial in reducing the number of output channels of the SiPM-based PET detector, showing a timing resolution comparable with the capacitive method.

In addition to the timing performance, it is also important to achieve a good energy performance of the PET detectors, since more scattered coincidence events can be rejected by narrowing the energy window at the 511 keV photopeak. Hence, it is recommended to estimate the energy information by fully integrating the area under the scintillation output signal, since the uncertainty of energy measurement can be reduced significantly by detecting more visible

Table 2. Performance evaluation results of light-sharing experiment.

<table>
<thead>
<tr>
<th>Detector configuration</th>
<th>DWR (a.u.)</th>
<th>$\text{d}E/E^a$ (%)</th>
<th>CRT (ps FWHM)</th>
<th>$\text{CV}_{\tau_1}$ (%)</th>
<th>$\text{CV}_{\tau_2}$ (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Light-sharing</td>
<td>Resistive</td>
<td>2.71</td>
<td>15.7 ± 1.6</td>
<td>596 ± 37</td>
<td>11.3</td>
</tr>
<tr>
<td></td>
<td>Capacitive</td>
<td>2.66</td>
<td>15.7 ± 1.5</td>
<td>556 ± 44</td>
<td>7.9</td>
</tr>
<tr>
<td></td>
<td>Hybrid</td>
<td>2.74</td>
<td>15.6 ± 1.4</td>
<td>555 ± 42</td>
<td>3.2</td>
</tr>
</tbody>
</table>

$^a \text{d}E/E$: energy resolution.
photons produced from the scintillation crystal. Therefore, in terms of the statistical error, it is expected that the resistive and hybrid charge division methods would yield more accurate energy measurements with little loss of charge information; in other words, the capacitive charge division method would result in the degradation of energy resolution as the width of integration window increases due to the negative charge integration caused by the undershoot response. However, in this study, the capacitive DPC yielded energy resolution comparable to both the resistive and hybrid DPCs, since the integration window was narrow enough (i.e. 150 ns) to avoid the negative charge integration from the capacitive DPC.

The charge-based readout requires an analog-to-digital converter (ADC) and a subsequent signal processing device to obtain the energy information of detected scintillation signal; and therefore, it usually demands a large amount of resource-heavy electronics and causes significant power consumption and heat dissipation. In this regard, time-based readout based on a counter or a time-to-digital converter (TDC) can be a cost-effective alternative because it enables a highly-integrated signal readout by simplifying the front-end and back-end electronics of PET system without the requirement of ADC. Conventionally, the charge division multiplexing method was not regarded as an eligible candidate for the time-based readout. Figures 8(a) and (b) demonstrate the imperfect calibration of the TOT data acquired from the conventional resistive and capacitive charge division methods, resulting in blurring and distortion of the flood histogram. In order to achieve the best performance of the time-based readout, therefore, it is important to obtain a uniform pulse shape of output signal from the multiplexing network so that the TOT highly correlates to the energy of detected photon. The proposed hybrid charge division method showed a strong correlation between the TOT and integrated charge of the position signal owing to its excellent pulse shape uniformity, as shown in figure 8(c); accordingly, the linearity calibration of TOT data can be performed by simple logarithmic regression, showing better quality of the flood histogram as compared with the conventional charge division methods as shown in figure 8(f). As a result, the hybrid charge division method can be useful for enhancing the performance of the time-based readout in terms of the linearity and calibration simplicity of the energy.

5. Summary and conclusion

In this study, we introduced the concept and demonstrated the feasibility of the hybrid charge division method using the DPC network. The simulation and experimental results were promising in that the hybrid charge division method yielded good performance in terms of energy and timing, inheriting only the advantages of the resistive and capacitive charge division methods. Moreover, the hybrid charge division method showed excellent pulse shape uniformity that does not depend on the position of the interacted crystal, and an outstanding feasibility for the time-based readout. Therefore, we can conclude that the hybrid charge division method is useful for effectively reducing the number of output channels of the SiPM array.

Acknowledgments

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References


